

PB-3600 JUMPER SETTING

- **J1 : CPU CLOCK SELECT**

5-6	3-4	1-2		
FSLA	FSLC	FSLB	CPU	PCI
ON	ON	ON	266.66	33
OFF	ON	ON	133.33	33
ON	ON	OFF	200	33
OFF	ON	OFF	166.66	33
OFF	OFF	ON	100	33
OFF	OFF	OFF	Reserved	

- **JBAT1 : CMOS DATA CONTROL**

Pin 1-2 Short : Normal

Pin 2-3 Short : Clear CMOS

- **JP2 : PS2 MOUSE LOOP BACK**

All Open : Through PS/2 Touch Controller

Pin 1-2 Short 、 Pin 3-4 Short : No PS/2 Touch Controller

- **JP3: EUP/WAKE ON OPTION**

OPEN: To support EUP But no support LAN/Alarm/modem wake up & power fail after power on function

Short : Disable EUP support but support LAN/Alarm/modem wake up & power fail after power on function

- **JP5 :COM1/COM2 DC SUPPLY SELECT**

Pin 1-3 Short : COM1 support 5V function

Pin 3-5 Short : COM1 Pin9 connected as RI

Pin 2-4 Short : COM2 support 5V function

Pin 4-6 Short : COM2 Pin9 connected as RI

- **JP6 :COM3/COM4 DC SUPPLY SELECT**

Pin 1-3 Short : COM3 support 5V function

Pin 3-5 Short : COM3 Pin1 connected as DCD

Pin 2-4 Short : COM4 support 5V function

Pin 4-6 Short : COM2 Pin1 connected as DCD

- **JP4 : MICRO Processor Firmware Update Header**

- **JP8 : COM1/COM2 DC SUPPLY SELECT**
 - Pin 1-3 Short : COM1 support 12V function
 - Pin 3-5 Short : COM1 Pin1 connected as DCD**
 - Pin 2-4 Short : COM2 support 12V function
 - Pin 4-6 Short : COM2 Pin1 connected as DCD**

- **JP7 : COM1/COM2 DC SUPPLY SELECT**
 - Pin 1-3 Short : COM3 support 12V function
 - Pin 3-5 Short : COM3 Pin1 connected as DCD**
 - Pin 2-4 Short : COM4 support 12V function
 - Pin 4-6 Short : COM4 Pin1 connected as DCD**

